ITU-T F.751.1 Assessment criteria for distributed ledger technology (DLT) platforms

Wei Kai Rapporteur Q22 of ITU-T SG16/CAICT

ITU-T FG DLT (2017-2019) ~ Spirit of collaboration





Functions of Standards

Function	Examples	Economic Impacts								
Specify quality and reliability	Performance metrics, such as minimum quality levels Procedures, such as equipment calibrations	Expand market share through performance assurance and reduction in transaction costs								
Provide information	Measurement and test methods Science and engineering data bases, standard reference materials	 Increased research efficiency through more accurate research inputs and verifiable research results Higher productivity and quality through better process control 								
	■ Terminology	 Reduced transaction costs and hence lower prices for new products 								
Assure Interoperability	Interconnection among system components	Achieve network externalities and thereby expand value/cost ratios								
	 Portability of software across implementations of a computer system⁵ 	Facilitate open systems and thereby enable more competition at component and subsystem levels								
Enable variety reduction	 Microprocessor architecture, size of silicon wafers 	Achieve economies of scale and compatibility across components								

Source: Tassey, Gregory. The Economic Nature of Knowledge Embodied in Standards for Technology-Based Industries, ResearchGate, January 2015



Gaps in DLT Standards



Gaps in standards



When to apply DLT

Assessment of DLT usefulness

Types of DLT – features and performance expectations

Functional risks

Heuristics for cross-platform comparison



Core technical elements

Off-chain networks (e.g. Lightning)

Consensus algorithms

Functional structure (e.g. blockchain, DAG)

DLT interoperability



DLT performance tests

Taxonomy performance tests (e.g. technical, functional, user, stress, security tests) and how they might be performed

Performance test requirements



Related verticals

Education

Sustainable development

Construction management

Rights management

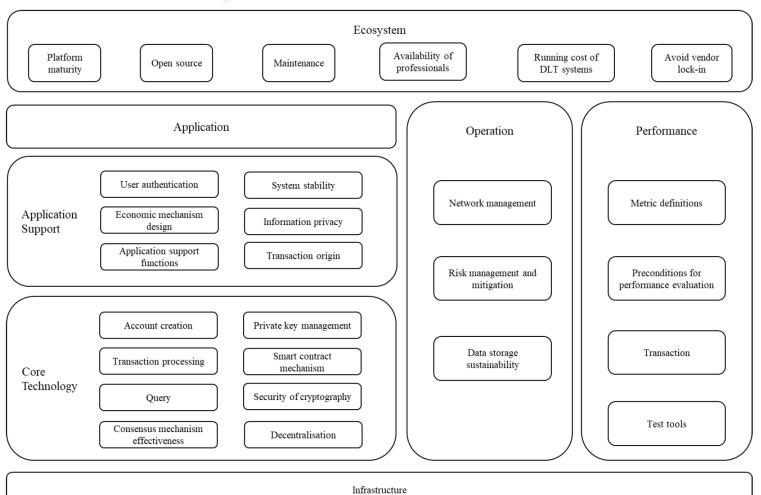
Land registries

Source: Global Standards Mapping Initiative: An overview of blockchain technical standards WHITE PAPER, October 2020



ITU-T F.751.1

A guideline for DLT platform assessment and information disclosure of a certain DLT platform product





ITU-T F.751.1 in the real world

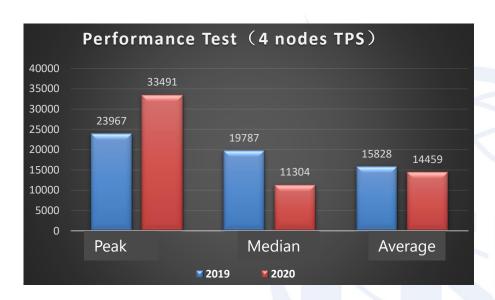
Mapping F.751.1 to Trusted Blockchain Initiatives(TBI) testing 2020

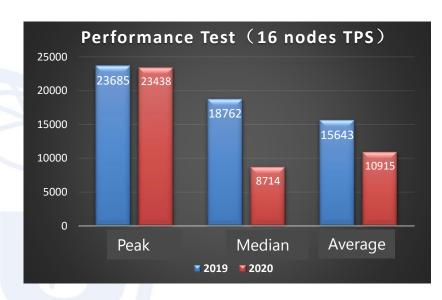
Mandate items for TBI testing

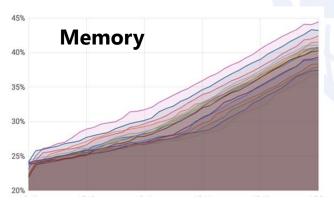
Optional items for TBI testing

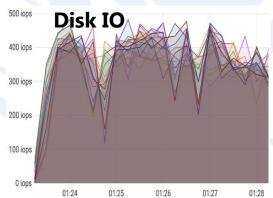
	9.3	8.4.1	7.4.1	7.4.1	7.7	7.4.1	9.2	7.6.2	8.5.2	7.6.4	8.2.1	7.2	7.1	8.1	7.5.1	8.5.3	9.1.1	9.1.2	9.1.1	7.61	7.4.2	9.1.3	9.3	7.7	7.6.3	9.1.3	8.2	9.1.4	8.2.6	8.4.3/ 8.4.2	8.2.2	9.2.3	7.5.2
ITU 751.1	Data storag e sustai nabilit y	Secure trans missio n	Data consis tency	consis	Securi ty of crypt ograp hy	Data consis tency	Risk manag ement and mitiga tion	ement of	User interfa ce for smart contra ct	Smart contra ct data access contro 1	ty for manag e	Trasac tion Proces sing	Accou nt Creati on	User authen ticatio n	Softw are wallet	Multi- langua ge softw are develo pment kits (SDK s)	status	Multi type nodes	Node status monit oring	Monit or ability of partici pants' status	BFT/ CFT	Node config uratio n modifi cation	Data storag e sustai nabilit y	Securi ty of crypt ograp hy	Securi ty of smart contra ct	Node config uratio n modifi cation	m	ork	tv for	n privac y/Rest	Stabili ty for cross- chain operat ion	point	Hard ware wallet
SUT A																					CFT	hot											
SUT B																					CFT	cold											
SUT C																					CFT	cold											
SUT D																					BFT	hot											
SUT E																					BFT	hot											
SUT F																					BFT	hot											
SUT G																					BFT	hot											
SUT H																					BFT	hot											
SUT I																					CFT	hot											
SUT J																					CFT	cold											
SUT K																					CFT	hot											
SUT L																					CFT	hot											
SUT M																					CFT	cold											
SUT N																					BFT												
SUT O																					CFT												
SUT P																					BFT												
SUT Q																					BFT	cold											
SUT R																					CFT	hot											
SUT W																					CFT	hot											
SUT T																					CFT	hot											
SUT U																					BFT	hot											
SUT V																					CFT	hot											
SUT W																					BFT	hot											
SUT X																					CFT	cold											
SUT Y																					BFT	cold											
SUT Z																					CFT	hot											
SUT AB																					BFT	cold											
SUT AC																					CFT	cold											
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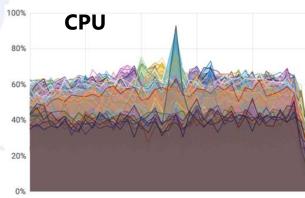
The performance of DLTs









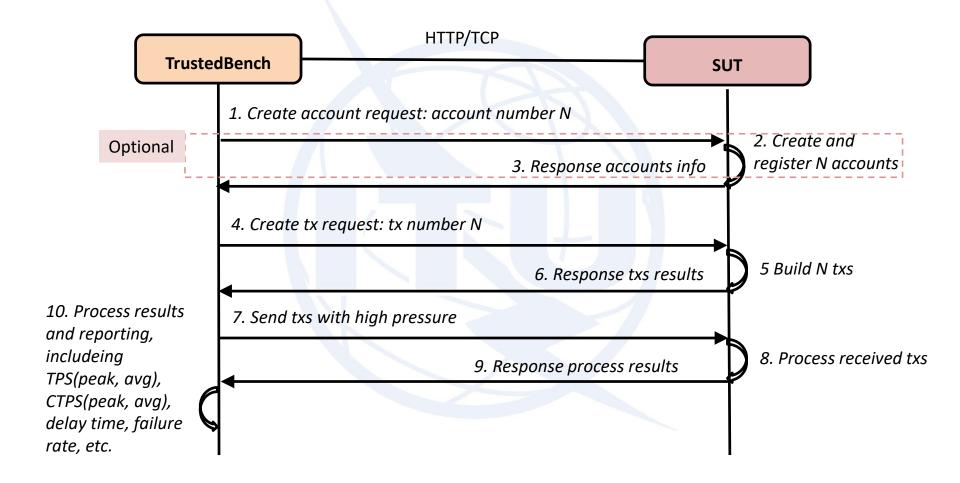


Machine: Dell R730 & Think Server RD450, CPU 24-core, 128G RAM; Network: 10G Ethernet

Source: CAICT, Dec 2020



TrustedBench flow





The way forward...

- 1. Functional Test specifications
- 2. Performance benchmark specifications



Thank you!

weikai@caict.ac.cn Rapporteur Q22 of ITU-T SG16/CAICT

